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1/6

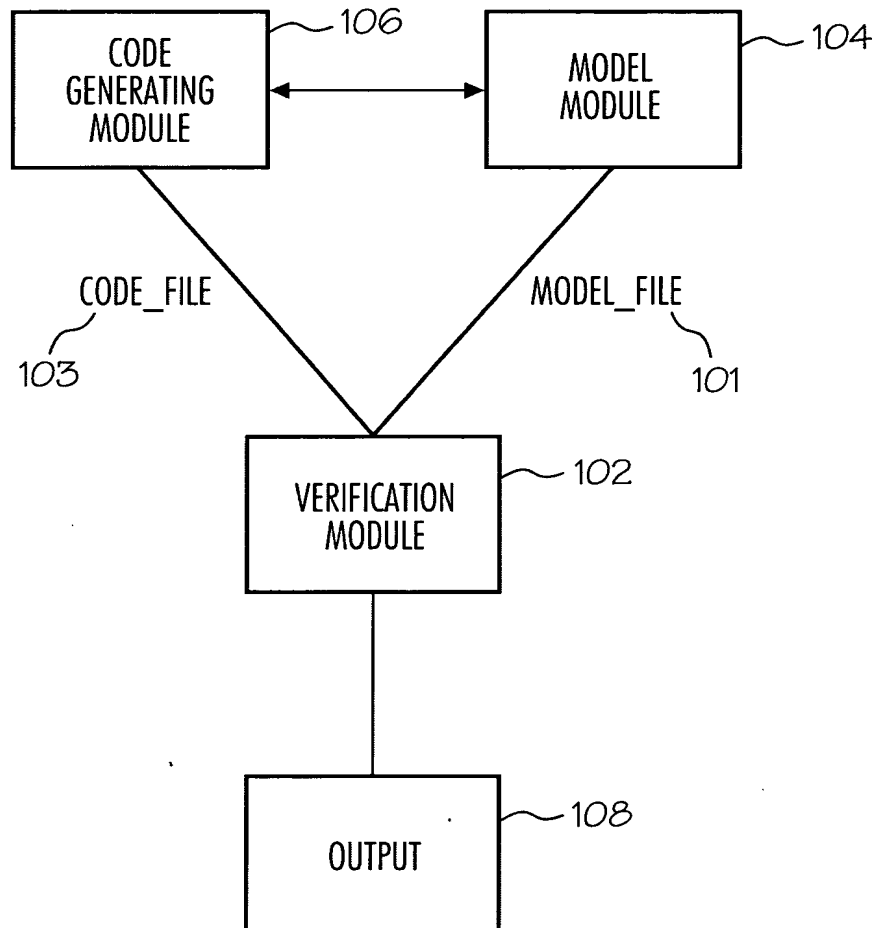


Fig. 1

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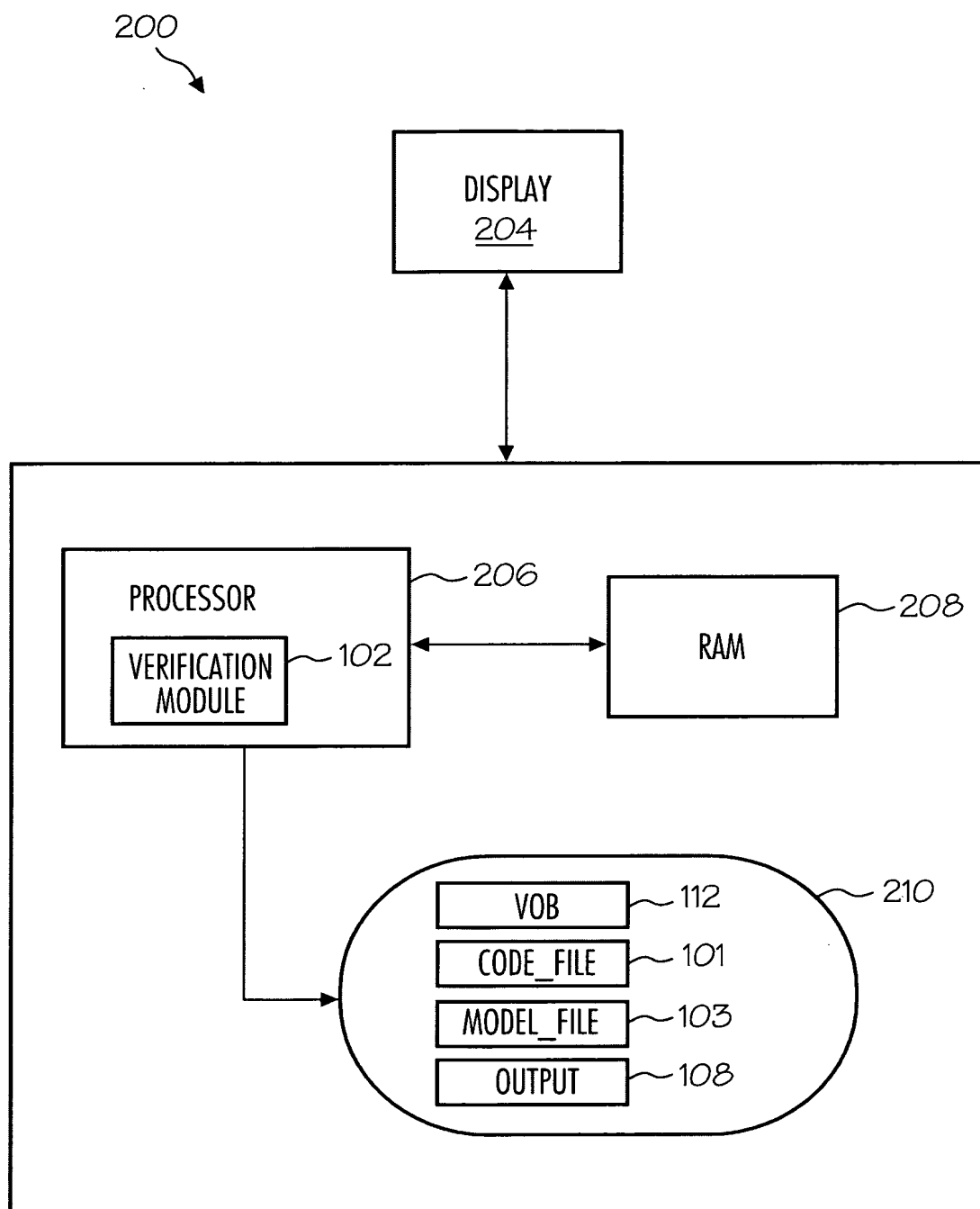


Fig. 2

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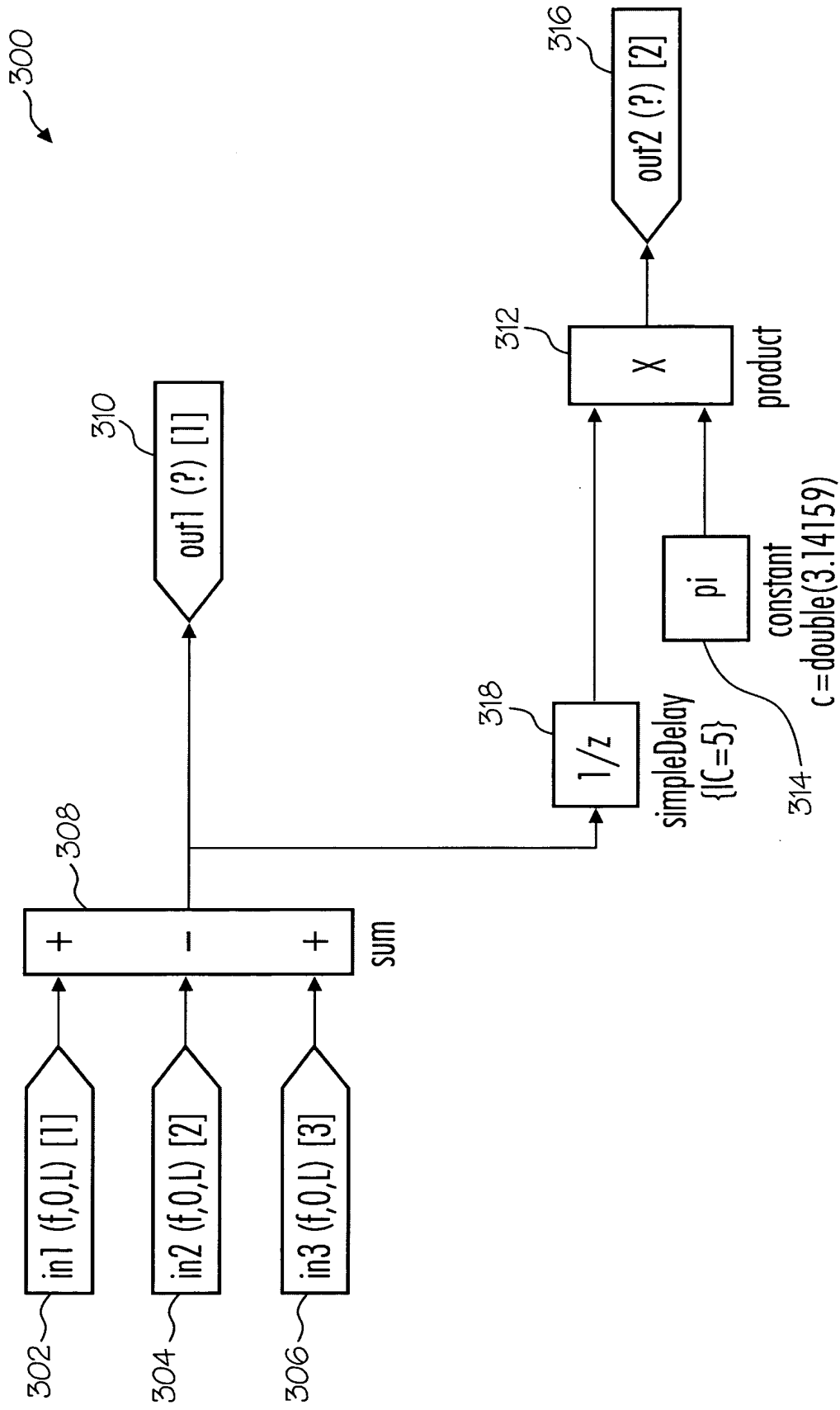


Fig. 3a

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```

Ccrdemo.c:
322 /*
    * COMPANY X PROPRIETARY AND CONFIDENTIAL
    * Copyright (c) 1996-2004 Company X International Inc.
    * Unpublished Work. All rights reserved.
    */

const char proprietaryNotice[] = " * COMPANY X PROPRIETARY AND CONFIDENTIAL * Copyright
(c) 1996-2004 Company X International Inc. * Unpublished Work. All rights reserved.";

/*
 * Real-Time Workshop code generation for Simulink model "ccrdemo.mdl".
 *
 * Model Version          : 1.4
 * Real-Time Workshop file version : 4.1.1 $Date: 2001/08/24 12:43:38 $
 * Real-Time Workshop file generated on : Mon Jan 19 12:12:31 2004
 * TLC version            : 4.1 (Oct 5 2001)
 * C source code generated on      : Mon Jan 19 12:12:31 2004
 */

#include "ccrdemo_private.h"
#include "ccrdemo.h"

/* Block parameters (auto storage) */
const Parameters_ccrdemo ccrdemo_P = {
    5.0, /*SimpleDelay_1_X0 : '<S3>/SimpleDelay_1' */
    3.14159 /*Constant_1_Value : '<S1>/Constant_1' */
};

/* Block signals (auto storage) */
BlockIO_ccrdemo ccrdemo_B;

/* Block states (auto storage) */
D_Work_ccrdemo ccrdemo_DWork;

/* External inputs (root inport signals with auto storage) */
ExternalInputs_ccrdemo ccrdemo_U;

/* External output (root outputs fed by signals with auto storage) */
ExternalOutputs_ccrdemo ccrdemo_Y;

/* Real-time object */
ccrdemo_RT_OBJECT ccrdemo_rtO;

```

Fig. 3b

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5/6

```

/* Model step function */
void ccrdemo_step(void)
{
    /* Sum Block: '<S4>/Sum_1' */ 342
    ccrdemo_B.Sum_1 = ccrdemo_U.in1 - ccrdemo_U.in2 + ccrdemo_U.in3;

    /* Output Block: '<Root>/out1' */
    ccrdemo_Y.out1 = ccrdemo_B.Sum_1;

    /* UnitDelay Block: '<S3>/SimpleDelay_1' */
    ccrdemo_B.SimpleDelay_1 = ccrdemo_DWork.SimpleDelay_1_DSTATE;

    /* Product Block: '<S2>/Product_1' */
    ccrdemo_B.Product_1 = ccrdemo_B.SimpleDelay_1 * ccrdemo_P.Constant_1_Value;

    /* Output Block: '<Root>/out2' */
    ccrdemo_Y.out2 = ccrdemo_B.Product_1;
}

/* UnitDelay Block: '<S3>/SimpleDelay_1' */
ccrdemo_DWork.SimpleDelay_1_DSTATE = ccrdemo_B.Sum_1;
}

/* Model initialize function */
void ccrdemo_initialize(boolean_T firstTime)
{
    if (firstTime) {
        /* registration code */
        ccrdemo_rt0.errorStatus = (void *)0;

        /* UnitDelay Block: '<S3>/SimpleDelay_1' */
        ccrdemo_B.SimpleDelay_1 = ccrdemo_P.SimpleDelay_1_X0;
    }

    /* UnitDelay Block: '<S3>/SimpleDelay_1' */
    ccrdemo_DWork.SimpleDelay_1_DSTATE = ccrdemo_P.SimpleDelay_1_X0;
}

```

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Fig. 3b (continued)

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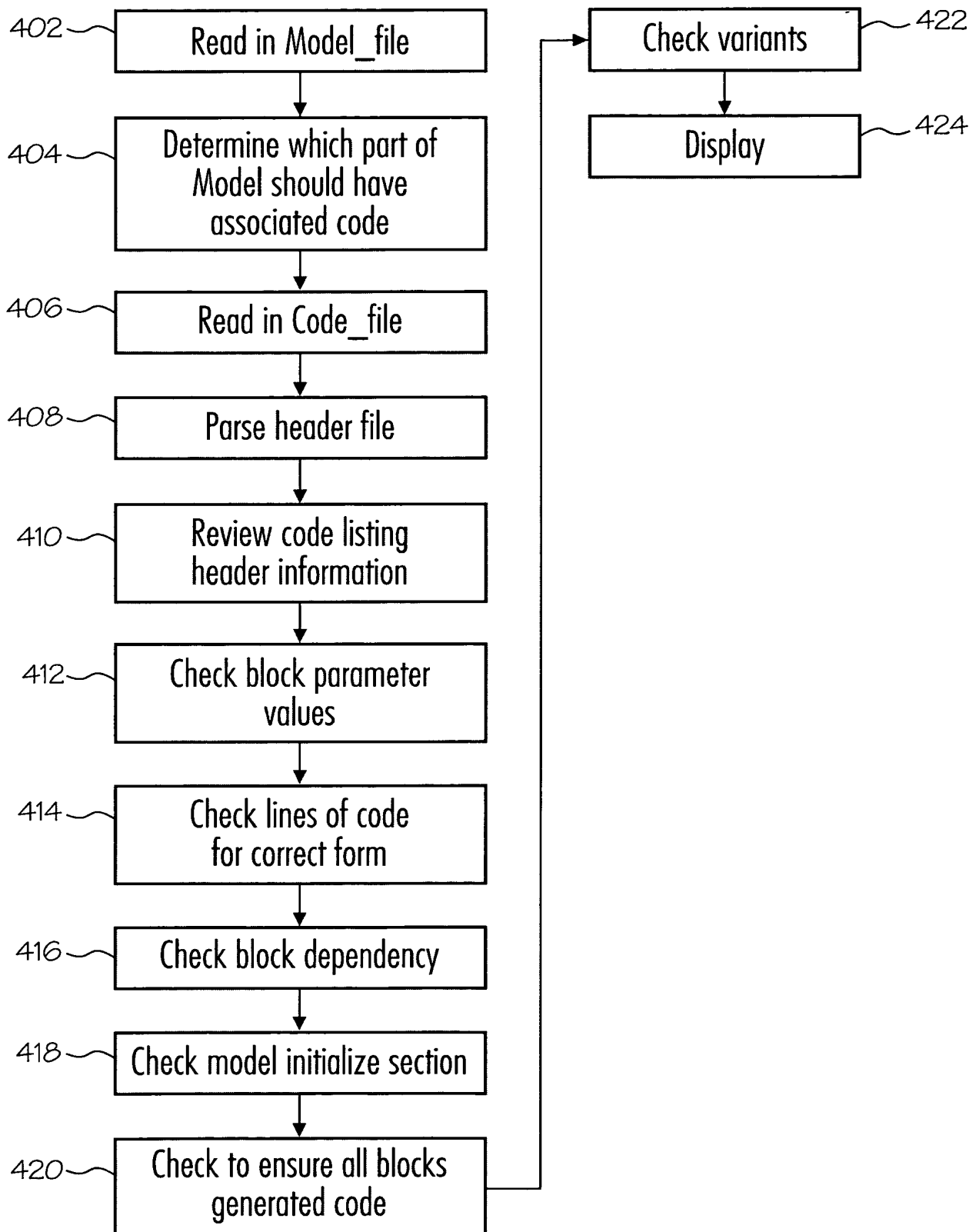


Fig. 4